



PIMF-02SN/TTL Mifare Module

Ver.12.1

● Specifications

Features	Mifare module (UID) with antenna
Dimensions	64(L) x 38(W) x 10(H) mm
Weight	9.4g \pm 5%

● Transmission Rate

Baud rate : 9600 bps

Parity bit : none

Data bit : 8

Stop bit : 1

● Reader

Host(Response)

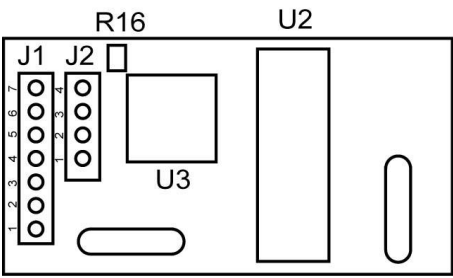
Command format :	STX	RAD	LEN	STATUS	DATA	LRC	ETX
Example: Original code:	02	01	02	00	01	02	03
ASCII code to be sent:	02h	30h, 31h	30h, 32h	30h, 30h	30h, 31h	30h, 32h	03h

STX	: Start code. 1 byte(02h)
RAD	: Reader Address. 1 byte(01H~FEh)
LEN	: Total length of STATUS & DATA
STATUS	: 1 byte. "00" is for success; "85" is for failure
DATA	: It should be followed by the contents of different command
LRC	: It's consisted of per Byte Exclusive OR from RAD to DATA. it's tantamount to $RAD \oplus LEN \oplus STATUS \oplus DATA 0 \oplus DATA 1 \dots \dots \dots \oplus DATA N$
ETX	: Ending Code. 1 byte(03h)

- ◎ Response Data Format : DATA as Command Data Format returns to ASCII.
- ◎ Take note of the contents between RAD and LRC, and mathematical calculations:
 - a. Excepting the response of STX & ETX, the code has been transferred to 2 Bytes of ASCII
For example : RAD =01 transfer to 30, 31
 - b. Take Byte mode to perform LRC's mathematical calculations
 - c. For example : Response of GET RAD is as below :
STX, RAD, LEN, STATUS, DATA, LRC, ETX
02, 01, 02, 00, 01, 02, 03
Response = 02, 30, 31, 30, 32, 30, 30, 30, 31, 30, 32, 03

Function	STATUS	DATA
Edit RAD	00 or 85	None
Get RAD	00 or 85	Address (1 byte)
Read Mifare S/N	00 or 85	Serial No.(4 bytes)
Read Data	00 or 85	16 bytes of the chosen block
Reset	00 or 85	None
Antenna On	00 or 85	None
Antenna Off	00 or 85	None
Load Key	00 or 85	None
Write Card Data	00 or 85	None
EDIT Reader Mode	00 or 85	None
Get Reader Mode	00 or 85	0A or 0B or 0C or 0D(1 byte), 0A:ABA, 0B:W26, 0C:W34, 0D:RS-232
EDIT Block No.	00 or 85	None
Get Block No.	00 or 85	(00~3Fh) (1 byte)

● Wire Configuration



J1			
Interface	TTL	Wiegand	ABA
Pin no	Wire assignment		
1.(+V)	+5V	+5V	+5V
2.(G)	GND	GND	GND
3.(TX)	TX	—	—
4.(RX)	RX	—	—
5.(D0)	—	D0	DATA
6.(D1)	—	D1	CLK
7.(A)	—	—	CLS(R24 short, R10Ω)

J2	
Interface	Wiegand
Pin no	Wire assignment
1.	GND
2.	+5V
3.	D1
4.	D0